

## **AMENDMENTS TO THE CLAIMS:**

### **Complete Listing of Claims**

1. (currently amended) A method for a device to download data from a data source while connected to a host, comprising the following steps performed by the device:
  - connecting to a host;
  - waiting for a request signal from the host;
  - responding to the request signal with a negative acknowledgement (NAK) to intentionally postpone a response to the request signal; and
  - downloading data from the data source for a predetermined time period based on the request signal.
2. (original) The method of claim 1, the data source comprising non-volatile memory.
3. (original) The method of claim 2, the non-volatile memory is at least one of an Electrically Erasable Programmable Read Only Memory (EEPROM) and a flash memory.
4. (original) The method of claim 2, further comprising reading a signature from the non-volatile memory and validating the signature prior to connecting to the host.
5. (original) The method of claim 2, further comprising reading descriptor information from the non-volatile memory prior to connecting to the host.
6. (original) The method of claim 1, further comprising setting a pointer for tracking data downloaded from the data source.

7. (original) The method of claim 1, further comprising repeating waiting for a request signal from the host, responding to the request signal with a NAK and downloading data from the data source for a predetermined time period based on the request signal, until the data downloaded from the data source has completed.
8. (original) The method of claim 7, further comprising updating a download pointer each time the predetermine time period is completed.
9. (original) The method of claim 8, the predetermined time period is a first time period for a data request signal, and a second time period for a status request signal.
10. (currently amended) The method of claim 9, the first time period is ~~about~~ five hundred milliseconds and the second time period is ~~about~~ fifty milliseconds.
11. (original) The method of claim 1, the host is a USB host and the device is a USB device.
12. (original) The method of claim 1, the predetermined time period is monitored by a timer.
13. (original) The method of claim 1, further comprising determining and downloading a number of data blocks to be downloaded based on the predetermined time period.
14. (original) The method of claim 13, the number of data blocks to be downloaded being further based on at least one of a download data rate and a block size.

15. (original) The method of claim 13, further comprising setting a loop counter based on the number of data blocks to be downloaded.

16. (currently amended) A method for a Universal Serial Bus (USB) device to download firmware while connected to a USB host, comprising the following steps performed by the device:

- connecting to a USB host;

- waiting for a request signal from the USB host;

- downloading data blocks associated with firmware from a data source based on a predetermined time period associated with the request signal type; and

- repeating the waiting for a request signal and the downloading data blocks from the data source, until the downloading of the firmware is complete.

17. (original) The method of claim 16, further comprising updating a download pointer that tracks the last data block downloaded.

18. (original) The method of claim 16, further comprising sending a negative acknowledgement (NAK) to the host to intentionally postpone the transmission of the response to the request signal.

19. (original) The method of claim 16, further comprising determining the signal request type and setting the predetermined time period accordingly.

20. (original) The method of claim 16, further comprising determining a number of data blocks to be downloaded based on the predetermined time period.

21. (original) The method of claim 20, the number of data blocks to be downloaded being further based on at least one of a download data rate and a block size.

22. (currently amended) A universal serial bus (USB) compatible device, comprising:

a non-volatile memory having firmware stored therein; ~~and~~

a microcontroller unit (MCU); and

an instruction memory storing instructions for execution by the MCU that upon reset configures the device, the execution of the instructions controlling such that the device to respond responds with a negative acknowledgement (NAK) in response to a request signal from a host controller, to download the firmware for use by the MCU for a period of time after responding with the NAK, and to continue to respond with NAKs and to download the firmware until downloading of the firmware to the MCU has completed.

23. (original) The device of claim 22, further comprising a memory for storing a download pointer to track the firmware download.

24. (original) The device of claim 22, the MCU downloads data blocks associated with the firmware for a predetermined time period based on the request signal type from the host controller.

25. (original) The device of claim 24, the MCU further determines the number of data blocks to be downloaded for the predetermined time period based on download data rate and a block size.

26. (original) The device of claim 24, the predetermined time period is a first time period for a signal with a data stage and a second time period for a signal without a data stage.

27. (currently amended) The device of claim 26, the first time period is ~~about~~ five hundred milliseconds and the second time period is ~~about~~ fifty milliseconds.

28. (original) The device of claim 23, further comprising a timer for monitoring the firmware download.